

IN THE CLAIMS:

Please amend claims 7, 23, and 24 as follows.

*D 17* 12. (Three Times Amended) A circuit comprising:  
a first circuit having a first input and a first output, wherein said first output  
includes a function of a signal at said first input and also includes a noise component  
resulting from noise experienced by said first circuit;  
a second circuit having a second input and a second output;  
a signal supplying circuit [suppyling] supplying to the second input a signal an  
inverse of the signal at the first input; and  
a third circuit having a third input connected to said first output and a fourth input  
connected to said second output, and subtracting said second output from said first  
output.

*D 2* 23. (Amended) A circuit according to claim 1, wherein said [third] subtractor  
circuit is digital.

*D 3* 24. (Amended) A circuit according to claim 1, wherein said [third] subtractor  
circuit is analog.